



Ing-Jer Huang

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Ing-Jer Huang received the BS degree in electrical engineering from National Taiwan University, Taiwan, R. O. C., in 1986, and the MS and Ph.D. degrees in computer engineering from the University of Southern California, U. S. A., in 1989 and 1994, respectively. He is currently a professor in the Department of Computer Science and Engineering at National Sun Yat-Sen University in Taiwan. He was a visiting professor at University of California, Irvine, USA, during 2008-2009.

His research interests include computer architecture, SoC platforms, design automation, system software, embedded systems, and hardware/software co-design/verification. He has established a long term solid relationship with IC related industries. Many of his techniques have been licensed to companies or institutes such as Andes, ACard, ADMtek, ATChip, Faraday, GlobalUniChip, Himax, Holtek, Microtime, RDC, Socle, ITRI, S&Q, Temento Systems, Via, etc.

During the recent years, he has been working on the development of SoC infrastructure and related IP's to support real time on-chip tracing, debugging, verification and monitoring for advanced on-chip connection. In addition, he is currently leading a large scale joint project consisting of eight professors in the development of the hardware/software of a 3D graphics/stereo SoC for consumer electronics, in which multidisciplinary research issues are explored, including computer graphics algorithms, application interface, middleware, compiler, device drivers, operating system, development environment, low power design, system modeling/exploration, hardware architecture, SoC integration/monitoring/debugging/testing, FPGA/chip implementation and verification, etc. Under his leadership, this team has won two Distinguished Joint Research Team of National Research Programs in 2010 and 2013, respectively.

He is Distinguished Professor and ASE Chair Professor at the university. He is currently the director of Digital Content and Multimedia Technology Research Center, and was the chairman of his department and the vice CEO of Operation Center for Industry and University Cooperation of the university. In addition, he has won many awards, including National Invention Award (2014), Excellent Professor in Engineering (2007), Chinese Institute of Engineers, Excellent Industrial Collaboration Award (2016) and Invention Awards (2005, 2007), National Sun Yat-Sen University, iENA/Geneva/Taipei Invention Awards (2010-2013), and Excellent Advisor, National Sun Yat-Sen University (1999).